

U.S. PTO  
10046077  
01/16/02

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PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10046077	01/16/2002	438	270	2842	

\*\*APPLICANTS: Inagawa Hiroshi; Machida Nobuo; Oishi Kentaro;  
2813

\*\*CONTINUING DATA VERIFIED: *By Wond*

\*\* FOREIGN APPLICATIONS VERIFIED: *By*

JAPAN 2001-042352 02/19/2001

PG-PUB	<input type="checkbox"/> DO NOT PUBLISH	<input type="checkbox"/> RESCIND	
Foreign priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO	
35 USC 119 conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	XA-9612	
Verified and Acknowledged Examiner's initials	<i>Pat</i>	TITLE : Insulated gate type semiconductor device and method for fabricating the same	
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims <input type="checkbox"/> Print Claim for 0.6
ISSUE FEE			DRAWING
Amount Due	Date Paid		Sheets Draw. <input type="checkbox"/> Figs. Draw. <input type="checkbox"/> Print Fig. <input type="checkbox"/>
TERMINAL		Primary Examiner	
DISCLAIMER		PREPARED FOR ISSUE	Application Examiner
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